



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of

**HAYASHI *et al.***

**Application Number: 09/976,052**

**Filed: OCTOBER 15, 2001**

**For: LEVEL SHIFT CIRCUIT AND**

**SEMICONDUCTOR INTEGRATED CIRCUIT**

**Attorney Docket No. HITA.0110**

Art Unit: 2816

Examiner:

NGUYEN, MINH T.

6/EKT  
T-Step 702  
2-26-03

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**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**PETITION FOR EXTENSION OF TIME**

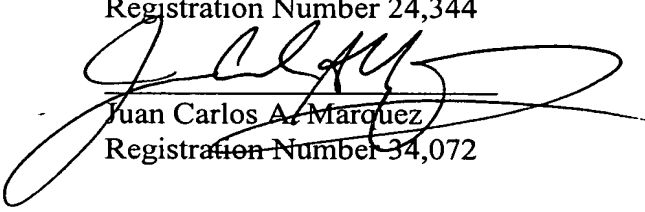
Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **two (2) months**. The fee set in 37 C.F.R. § 1.17 for the 2-month extension of time is **\$410.00**.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Respectfully submitted,

Stanley P. Fisher  
Registration Number 24,344

  
Juan Carlos A. Marquez  
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**March 14, 2003**

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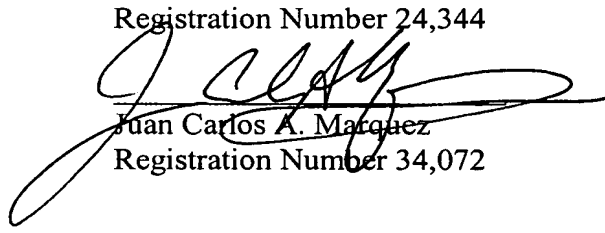
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